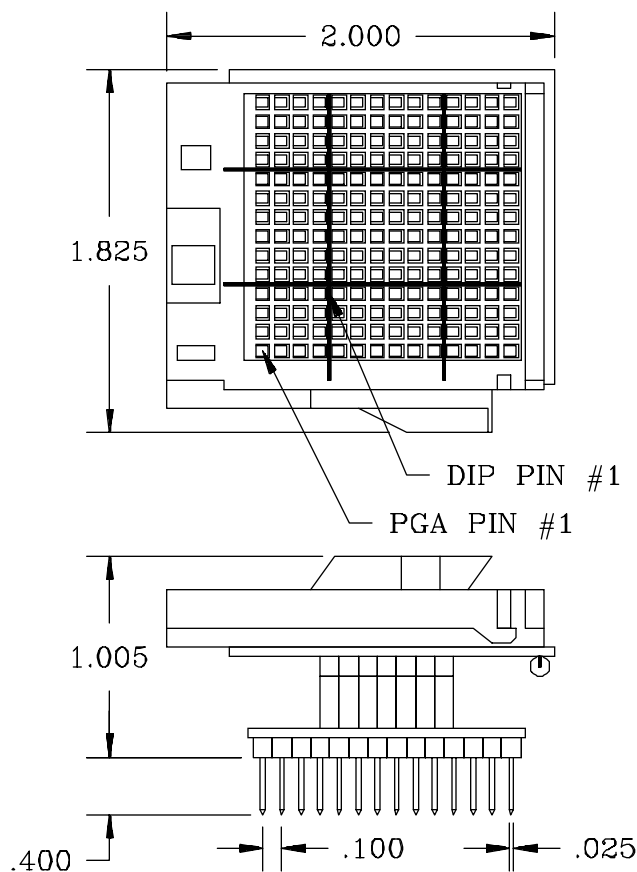# PROGRAMMING ADAPTER SPECIFICATION

Item: 1322801PG600
Description: AS-132-28-01PG-6

REPLACEMENT SOCKET: S-PGA-14-133-AZ
Call Emulation Technology for a replacement socket price and delivery quote.

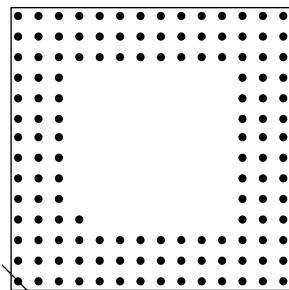## TYPICAL DIMENSIONS:

Note: DIP base is .600" wide

2.000
1.825
1.005
.400
.100
.025

DIP PIN #1
PGA PIN #1

All Dimensions in Inches.

(Assembly Drawing: F1875)

PACKAGE SPECIFICATIONS:

ET Footprint Code: 133-PGA8

CONNECTION TABLE:

132-Lead PGA SOCKET	PIN Names	28-Pin DIP Base
J14	SCLK	1
J1	SDI	2
P6	MODE	3
H2	ispEN	4
A6	VIEen	5
F1	VIE	6
C6	VEC0	7
D2	VEC1	8
D1	VEC2	9
E3	VEC3	10
E2	VEC4	11
E1	VEC5	12
F3	VEC6	13
B2, B8, B13, C8, H3, H12, M8, N2, N8	GND	14
P8	SDO	15
J3	VEC7	16
N6	VEC8	17
B9	VEC9	18
H1	RESET	19
G1	Y0 or IO1	20
G14	Y1 or IO2	21
H13	Y2 or IO3	22
H14	Y3 or IO4	23
N14	IO37 or IO5	24
M13	IO38 or IO6	25
L12	IO39 or IO7	26
M14	IO40 or IO8	27
C7, G2, G3, G12, G13, M7, N7	VCC	28

Notes:
1. Decoupling capacitor of 0.1uF for VCC/GND.
2. Decoupling capacitor of 0.01uF for ispEN/GND.
3. DIP pins 5 and 6 to the DUT have 1 resistor of 2K ohmns each.
4. VECs are for pLSI.
5. All pins not listed are not connected.

Compatible PGA devices:
ispLSI 1048 and pLSI 1048
ispLSI 1048C and pLSI 1048C
ispLSI 1048E and pLSI 1048E
ispLSI 2096 and pLSI 2096

Manufacturer:
Lattice

ET

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Date: 4/5/00
Rev: B
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